



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eiji Nishibe et al.

Art Unit : 2826

Serial No. : 10/007,384

Examiner : Tran, Tan N.

Filed : October 22, 2001

Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents

Washington, D.C. 20231

#41 election
and
3/27/03

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed February 25, 2003, applicant elects the invention of Group I (claims 1 to 4) drawn to a semiconductor device. The election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/14/03

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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